

Title (en)  
TECHNIQUES FOR IMPROVING GATE CONTROL OVER TRANSISTOR CHANNEL BY INCREASING EFFECTIVE GATE LENGTH

Title (de)  
VERFAHREN ZUR VERBESSERTEN GATE-ANSTEUERUNG ÜBER EINEN TRANSISTORKANAL DURCH ERHÖHUNG DER EFFEKTIVEN GATE-LÄNGE

Title (fr)  
TECHNIQUES PERMETTANT D'AMÉLIORER UNE COMMANDE DE GRILLE SUR UN CANAL DE TRANSISTOR PAR AUGMENTATION DE LA LONGUEUR DE GRILLE EFFECTIVE

Publication  
**EP 3084835 A1 20161026 (EN)**

Application  
**EP 13899916 A 20131218**

Priority  
US 2013076232 W 20131218

Abstract (en)  
[origin: WO2015094244A1] Techniques are disclosed for improving gate control over the channel of a transistor, by increasing the effective electrical gate length (Leff) through deposition of a gate control layer (GCL) at the interfaces of the channel with the source and drain regions. The GCL is a nominally undoped layer (or substantially lower doped layer, relative to the heavily doped S/D fill material) that can be deposited when forming a transistor using replacement S/D deposition. The GCL can be selectively deposited in the S/D cavities after such cavities have been formed and before the heavily doped S/D fill material is deposited. In this manner, the GCL decreases the source and drain underlap (Xud) with the gate stack and further separates the heavily doped source and drain regions. This, in turn, increases the effective electrical gate length (Leff) and improves the control that the gate has over the channel.

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**H01L 29/401** (2013.01 - EP KR US); **H01L 29/42392** (2013.01 - EP US); **H01L 29/66545** (2013.01 - EP KR US);  
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