

Title (en)

DELIMITATION FOR THE DEPOSITION OF ELECTRICAL COMPONENTS ON A BASE

Title (de)

BEGRENZUNG FÜR DAS ABLEGEN VON ELEKTRONISCHEN BAUTEILEN AUF EINE UNTERLAGE

Title (fr)

LIMITATION POUR POSER DES COMPOSANTS ÉLECTRONIQUES SUR UN SUPPORT

Publication

EP 3092881 A1 20161116 (DE)

Application

EP 15793686 A 20151028

Priority

- AT 507822014 A 20141029
- AT 2015050269 W 20151028

Abstract (en)

[origin: WO2016065383A1] Deposition table (1) for the inspection and/or orientation of electronic components (B) prior to populating a circuit board with said components, on which table the components, which are initially held at a nozzle end (7) by means of negative pressure, are deposited after release by the nozzle end, wherein the table (1) has a transparent deposition plate (2), preferably a glass plate, and a delimiting border (5, 6p, 6z, 12) is provided which prevents displacement of a component (B) temporarily deposited on the deposition plate by the air flowing out of the nozzle, and an optical inspection device is arranged under the deposition table (6u) and an optical inspection device is arranged above the deposition table (6o).

IPC 8 full level

H05K 13/08 (2006.01)

CPC (source: AT CN EP US)

H05K 13/022 (2013.01 - AT); **H05K 13/0409** (2018.07 - EP US); **H05K 13/08** (2013.01 - AT CN US); **H05K 13/0813** (2018.07 - EP US); **Y10T 29/49131** (2015.01 - AT); **Y10T 29/49133** (2015.01 - AT); **Y10T 29/53261** (2015.01 - AT)

Citation (search report)

See references of WO 2016065383A1

Designated contracting state (EPC)

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Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

WO 2016065383 A1 20160506; AT 516417 A1 20160515; AT 516417 B1 20181015; CN 106165563 A 20161123; CN 106165563 B 20190409; EP 3092881 A1 20161116; JP 2017514306 A 20170601; US 10206319 B2 20190212; US 2017049016 A1 20170216

DOCDB simple family (application)

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