

Title (en)
DELTA-SIGMA MODULATOR

Title (de)
DELTA-SIGMA-MODULATOR

Title (fr)
MODULATEUR DELTA-SIGMA

Publication
EP 3095195 A1 20161123 (DE)

Application
EP 15700313 A 20150114

Priority
• DE 102014200856 A 20140117
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Abstract (en)
[origin: WO2015107091A1] Embodiments provide a delta-sigma modulator for delivering an output signal on the basis of an input signal, comprising an integrator circuit, a first digital-analogue converter, a second digital-analogue converter, an analogue-digital converter and a control circuit. The integrator circuit is designed to deliver the output signal to an integrator output. The first digital-analogue converter (DAC) is coupled to an integrator input and designed to deliver a current on the basis of a first digital signal. The second digital-analogue converter (DAC) is coupled to an integrator output and designed to deliver a current on the basis of a second digital signal. The analogue-digital converter is coupled to the integrator output and designed to deliver the output signal in a digital form. The control circuit is coupled to the analogue-digital converter and designed to generate the first digital signal and the second digital signal on the basis of the digital output signal. The first digital signal describes pulses, and between the pulses the digital signal reverts to a zero value. The second digital signal describes an overlaying of pulses and a variable offset that follows the input signal.

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CPC (source: EP)
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