

Title (en)

PARALLEL CACHING ARCHITECTURE AND METHODS FOR BLOCK-BASED DATA PROCESSING

Title (de)

PARALLEL-CACHING-ARCHITEKTUR UND VERFAHREN ZUR BLOCKBASIERTEN DATENVERARBEITUNG

Title (fr)

ARCHITECTURE DE MISE EN ANTÉMÉMOIRE PARALLÈLE ET PROCÉDÉ DE TRAITEMENT DE DONNÉES À BASE DE BLOCS

Publication

EP 3101876 A1 20161207 (EN)

Application

EP 16172708 A 20160602

Priority

US 201514728973 A 20150602

Abstract (en)

A multi-processor computer system with shared memory resources includes a first plurality of sensors (120) configured to acquire inertial and positional data related to a mobile platform. The system further includes a first plurality of co-processors (110) having a hardware logic configured to control the acquisition of the inertial and positional data and configured to analyze the acquired data. The system also includes a second plurality of sensors (103) configured to acquire input data related to the mobile platform connected to a second plurality of co-processors (107) having a hardware logic configured to receive a plurality of streams of input data from the second plurality of sensors and configured to segment the input data into a plurality of discrete data segments. The system also includes a plurality of hardware processing units (134) configured to perform calculations related to the input data using the plurality of data segments.

IPC 8 full level

G06F 9/46 (2006.01); **G06F 9/48** (2006.01); **H04L 29/08** (2006.01)

CPC (source: EP US)

G06F 3/0604 (2013.01 - US); **G06F 3/064** (2013.01 - US); **G06F 3/0673** (2013.01 - US); **G06F 9/462** (2013.01 - EP US); **G06F 9/4887** (2013.01 - EP US); **G06F 12/084** (2013.01 - US); **G06F 12/0842** (2013.01 - US); **G06F 12/0875** (2013.01 - US); **G06F 2212/60** (2013.01 - US); **Y02D 10/00** (2017.12 - EP US)

Citation (search report)

- [I] US 2014361905 A1 20141211 - SADASIVAM SHANKAR [US], et al
- [A] US 2009066641 A1 20090312 - MAHAJAN SATYENDER [US], et al
- [A] US 2010287161 A1 20101111 - NAQVI WASEEM [US]
- [A] "Apple M7", 3 February 2014 (2014-02-03), XP002762634, Retrieved from the Internet <URL:https://web.archive.org/web/20140203180041/http://de.wikipedia.org/wiki/Apple_M7> [retrieved on 20160929]
- [A] "Apple iPhone 5s", 25 April 2014 (2014-04-25), XP002762635, Retrieved from the Internet <URL:https://web.archive.org/web/20140425065439/https://de.wikipedia.org/wiki/IPhone_5s> [retrieved on 20160929]

Cited by

CN110336809A; CN111524177A; CN110651328A; CN111176580A; CN107590609A; CN109634729A; US11138052B2; WO2019000363A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

EP 3101876 A1 20161207; **EP 3101876 B1 20220727**; US 2016357668 A1 20161208; US 9959208 B2 20180501

DOCDB simple family (application)

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