

Title (en)

CIRCUIT FOR GENERATING A REFERENCE VOLTAGE

Title (de)

SCHALTKREIS ZUR ERZEUGUNG EINER REFERENZSPANNUNG

Title (fr)

CIRCUIT DE GÉNÉRATION D'UNE TENSION DE RÉFÉRENCE

Publication

EP 3176669 B1 20190109 (FR)

Application

EP 16200987 A 20161128

Priority

FR 1561551 A 20151130

Abstract (en)

[origin: US2017153659A1] An FDSOI reference voltage generation circuit, including a CTAT current generation circuit; a PTAT-type voltage generation circuit including a first branch including first and second series-connected transistors, the front surface gates of the first and second transistors being connected to the conduction node of the second transistor opposite to the first transistor; a third diode-assembled transistor having a conduction node connected to an output node of the PTAT voltage generation circuit and having its other conduction node forming a reference voltage supply node; and a current mirror; wherein the first and second transistors are of LVT type and the third transistor is of RVT type.

IPC 8 full level

G05F 3/24 (2006.01); **G05F 3/26** (2006.01)

CPC (source: EP US)

G05F 3/242 (2013.01 - EP US); **G05F 3/267** (2013.01 - EP US)

Cited by

CN109698688A; US2021173421A1; US11940825B2

Designated contracting state (EPC)

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DOCDB simple family (publication)

EP 3176669 A1 20170607; **EP 3176669 B1 20190109**; US 10037047 B2 20180731; US 2017153659 A1 20170601

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EP 16200987 A 20161128; US 201615365039 A 20161130