Title (en)

METHOD FOR LOADING EXECUTABLE PROGRAM INSTRUCTIONS INTO A CHIP CARD DURING ACTIVE OPERATION

Title (de)

VERFAHREN ZUM LADEN VON AUSFÜHRBAREN PROGRAMMINSTRUKTIONEN IN EINE CHIPKARTE IM WIRKBETRIEB

Title (fr)

PROCÉDÉ POUR CHARGER DES INSTRUCTIONS DE PROGRAMME EXÉCUTABLES DANS UNE CARTE À PUCE OPÉRATIONNELLE

Publication

EP 3204850 A1 20170816 (DE)

Application

EP 15778904 A 20151001

Priority

- DE 102014220616 A 20141010
- EP 2015072746 W 20151001

Abstract (en)

[origin: WO2016055358A1] The invention relates to a method for loading executable program instructions (150) into a chip card (100) during active operation, wherein the chip card has at least one ROM (102) and one NVM (104), wherein a boot loader (106) for loading a chip card operating system (128) from a personalizing system (110) into the NVM prior to establishing active operation of the chip card is stored in the ROM, and the chip card operating system (128) is stored in the NVM, and wherein the boot loader is in a deactivated state during active operation of the chip card, wherein the boot loader in the deactivated state thereof can be started exclusively by the chip card operating system (128, 128'), wherein the method has the following steps: – coupling power into the chip card (100) from a chip card terminal (144), – starting the execution of the chip card operating system stored in the NVM by the chip card, – receiving a command (156) from the chip card terminal to load the executable program instructions by the chip card operating system, – authenticating the chip card terminal with respect to the chip card by the chip card operating system, – checking the authorization of the chip card terminal for loading the executable program instructions by the chip card operating system, – storing data in a predefined memory area (122) of the NVM, which display a successful implementation of the authentication and of the check, by the chip card operating system, – starting the execution of the boot loader by the chip card operating system and terminating the execution of the chip card operating system after the start of the boot loader, – reading the data from the predefined memory area by the boot loader, – loading the program instructions from the chip card terminal into the NVM by the boot loader under the precondition that the data in the predefined memory area display the successful authentication and check.

IPC 8 full level

G06F 9/445 (2006.01); **G06F 9/44** (2006.01)

CPC (source: EP US)

G06F 3/0622 (2013.01 - US); **G06F 3/0632** (2013.01 - US); **G06F 8/654** (2018.01 - EP US); **G06F 9/4406** (2013.01 - EP US); **G06F 12/1408** (2013.01 - US); **G06F 12/1458** (2013.01 - US); G06F 2212/402 (2013.01 - US)

Citation (search report)

See references of WO 2016055358A1

Citation (examination)

- DE 102011111698 A1 20130228 - FUJITSU TECH SOLUTIONS IP GMBH [DE]
- EP 2590383 A1 20130508 - RESEARCH IN MOTION LTD [CA]
- EP 2634693 A1 20130904 - MIMOON GMBH [DE]

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

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DOCDB simple family (publication)

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DOCDB simple family (application)

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