

Title (en)
SECURE ELECTRONIC ENTITY, ELECTRONIC APPARATUS AND METHOD FOR VERIFYING THE INTEGRITY OF DATA STORED IN SUCH A SECURE ELECTRONIC ENTITY

Title (de)
SICHERE ELEKTRONISCHE ENTITÄT, ELEKTRONISCHE VORRICHTUNG UND VERFAHREN ZUR VERIFIZIERUNG DER INTEGRITÄT VON GESPEICHERTEN DATEN IN EINER DERARTIGEN SICHEREN ELEKTRONISCHEN ENTITÄT

Title (fr)
ENTITÉ ÉLECTRONIQUE SÉCURISÉE, APPAREIL ÉLECTRONIQUE ET PROCÉDÉ DE VÉRIFICATION DE L'INTÉGRITÉ DE DONNÉES MÉMORISÉES DANS UNE TELLE ENTITÉ ÉLECTRONIQUE SÉCURISÉE

Publication
EP 3238200 A1 20171101 (FR)

Application
EP 15828654 A 20151217

Priority
• FR 1463256 A 20141223
• FR 2015053595 W 20151217

Abstract (en)
[origin: WO2016102833A1] The invention relates to a secure electronic entity (E) comprising a memory unit (NV) storing data in the form of multi-plets and a processing module (M) designed to receive data from an electronic device (TP). The processing module (M) is designed to determine a proof-of-integrity element in accordance with the data received and at least one portion of the stored multi-plets, and to transmit the proof-of-integrity element to the electronic device (TP). The invention also describes a method for verifying the integrity of data stored in such a secure electronic entity (E).

IPC 8 full level
G09C 1/00 (2006.01); **G06F 21/52** (2013.01); **H04L 9/06** (2006.01); **H04L 9/32** (2006.01)

CPC (source: EP KR US)
G06F 21/44 (2013.01 - EP US); **G06F 21/52** (2013.01 - KR); **G06F 21/602** (2013.01 - EP US); **G06F 21/64** (2013.01 - KR); **G09C 1/00** (2013.01 - EP KR US); **H04L 9/006** (2013.01 - US); **H04L 9/0631** (2013.01 - US); **H04L 9/0637** (2013.01 - EP KR US); **H04L 9/14** (2013.01 - US); **H04L 9/30** (2013.01 - US); **H04L 9/3242** (2013.01 - EP KR US); **H04L 9/3247** (2013.01 - US); **H04W 12/106** (2021.01 - EP US)

Citation (search report)
See references of WO 2016102833A1

Citation (examination)
• US 2012290870 A1 20121115 - SHAH YOGENDRA C [US], et al
• DWAIN CLARKE ET AL: "Checking the Integrity of Memory in a Snooping-Based Symmetric Multiprocessor (SMP) System", 26 July 2004 (2004-07-26), XP055517576, Retrieved from the Internet <URL:http://csg.csail.mit.edu/pubs/memos/Memo-470/smpMemoryMemo.pdf>
• TCG: "TCG Specification Architecture Overview, Specification Revision 1.2", TCG SPECIFICATION ARCHITECTURE OVERVIEW, TRUSTED COMPUTING GROUP, US, 28 April 2004 (2004-04-28), pages 1 - 54, XP002413737

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)
BA ME

DOCDB simple family (publication)
FR 3030831 A1 20160624; **FR 3030831 B1 20180302**; EP 3238200 A1 20171101; KR 20170097771 A 20170828; US 2017353315 A1 20171207; WO 2016102833 A1 20160630

DOCDB simple family (application)
FR 1463256 A 20141223; EP 15828654 A 20151217; FR 2015053595 W 20151217; KR 20177020623 A 20151217; US 201515538709 A 20151217