

Title (en)

SYSTEM AND METHOD FOR DYNAMICALLY BIASING OSCILLATORS FOR OPTIMUM PHASE NOISE

Title (de)

SYSTEM UND VERFAHREN ZUR DYNAMISCHEN VORSPANNUNG VON OSZILLATOREN FÜR OPTIMALES PHASENRAUSCHEN

Title (fr)

SYSTÈME ET PROCÉDÉ DE POLARISATION DYNAMIQUE D'OSCILLATEURS POUR UN BRUIT DE PHASE OPTIMUM

Publication

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Application

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Abstract (en)

[origin: WO2016111761A1] Systems and methods for biasing frequency oscillators to minimize phase noise are disclosed. The system may comprise a tank circuit having an inductor, at least a first coupling capacitor and a second coupling capacitor. The system may further comprise a varactor circuit electrically connected to the first coupling capacitor and the second coupling capacitor. The system may further comprise at least one first metal oxide semiconductor (MOS) device electrically connected in shunt with the tank circuit and a bias voltage. The at least one first MOS device may be electrically connected to a first gate bias voltage configured to bias the at least one first MOS device such that a first gate-to-source voltage of the at least one first MOS device remains below the first threshold voltage.

IPC 8 full level

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Citation (search report)

See references of WO 2016111761A1

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