

Title (en)
DISPLAY APPARATUS AND METHOD FOR CONTROLLING THE SAME

Title (de)
ANZEIGEVORRICHTUNG UND VERFAHREN ZUR STEUERUNG DAVON

Title (fr)
APPAREIL D'AFFICHAGE ET SON PROCÉDÉ DE COMMANDE

Publication
EP 3259751 B1 20210519 (EN)

Application
EP 16807831 A 20160610

Priority
• US 201562173582 P 20150610
• KR 20150173468 A 20151207
• KR 2016006152 W 20160610

Abstract (en)
[origin: KR20160145471A] Disclosed is a display apparatus. The display apparatus includes: a display panel provided at a position where data lines and gate lines cross to each other and having a plurality of pixels; a source driver IC arranged at one side surface of the display panel to apply a data voltage to the data lines; a gate driver IC arranged at one side surface of both side surfaces adjacent to the one surface of the display panel to apply a gate driving voltage to the gate lines; a timing controller for controlling a timing to apply the data voltage and the gate driving voltage; and a control unit fed back the gate driving voltage applied to at least one of the plurality of pixels, for detecting distortion of the gate driving voltage applied to the at least one pixel on the basis of the feedback voltage, and applying the gate driving voltage to the gate line by adjusting intensity of the gate driving voltage applied to the gate lines to compensate for the distorted gate driving voltage.

IPC 8 full level
G09G 3/36 (2006.01); **G09G 3/34** (2006.01)

CPC (source: EP US)
G09G 3/3406 (2013.01 - US); **G09G 3/342** (2013.01 - EP); **G09G 3/3648** (2013.01 - US); **G09G 3/3677** (2013.01 - EP);
G09G 2310/08 (2013.01 - US); **G09G 2320/0223** (2013.01 - EP US); **G09G 2320/0233** (2013.01 - EP US)

Citation (examination)
US 2014327708 A1 20141106 - YAMAKAWA RYO [JP]

Cited by
WO2023216597A1

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

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