

Title (en)

METHOD FOR ERROR HANDLING AND PARTIAL REDUNDANCY IN PARALLEL INVERTERS BY MEANS OF INPUT SWITCHES

Title (de)

VERFAHREN ZUR FEHLERBEHANDLUNG UND TEILREDUNDANZ IN PARALLELEN WECHSELRICHTERN MITTELS EINGANGSSCHALTERN

Title (fr)

PROCÉDÉ DE TRAITEMENT DES DÉFAUTS ET REDONDANCE PARTIELLE DANS DES ONDULEURS EN PARALLÈLE AU MOYEN DE COMMULATEURS D'ENTRÉE

Publication

EP 3284164 A1 20180221 (DE)

Application

EP 15716040 A 20150413

Priority

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Abstract (en)

[origin: WO2016165730A1] The invention relates to error handling in an inverter device (15) for inverting direct current from direct current generators (13, 14) into alternating current, wherein the inverter device (15) comprises a plurality of parallel direct current branches (16, 17), wherein each direct current branch (16, 17) has an inverter (11, 12) and a direct current input (18, 19) for connection to one of the direct current generators (13, 14). As a consequence of an error in one of the inverters (11, 12) detected by the inverter device (15), the direct current input (18, 19) of the faulty inverter is connected to the direct current input of an error-free inverter.

IPC 8 full level

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CPC (source: EP US)

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Citation (search report)

See references of WO 2016165730A1

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