Title (en)

PROCESSOR WITH EFFICIENT MEMORY ACCESS

Title (de)

PROZESSOR MIT EFFIZIENTEM SPEICHERZUGRIFF

Title (fr)

PROCESSEUR DOTÉ D'UN ACCÈS À LA MÉMOIRE EFFICACE

Publication

EP 3320428 A1 20180516 (EN)

Application

EP 16820923 A 20160704

Priority

- US 201514794835 A 20150709
- US 201514794837 A 20150709
- US 201514794841 A 20150709
- US 201514794853 A 20150709
- IB 2016053999 W 20160704

Abstract (en)

[origin: WO2017006235A1] A method includes, in a processor (20), processing program code that includes memory- access instructions, wherein at least some of the memory-access instructions include symbolic expressions that specify memory addresses in an external memory (41) in terms of one or more register names. A relationship between the memory addresses accessed by two or more of the memory-access instructions is identified, based on respective formats of the memory addresses specified in the symbolic expressions. An outcome of at least one of the memory-access instructions is assigned to be served from an internal memory (50) in the processor, based on the identified relationship.

IPC 8 full level

G06F 9/38 (2018.01); **G06F 9/34** (2018.01)

CPC (source: EP)

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Designated contracting state (EPC)

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