

Title (en)

SUBSTRATE FOR ELECTRICAL CIRCUITS AND METHOD FOR PRODUCING SUCH A SUBSTRATE

Title (de)

SUBSTRAT FÜR ELEKTRISCHE SCHALTKREISE UND VERFAHREN ZUR HERSTELLUNG EINES DERARTIGEN SUBSTRATES

Title (fr)

SUBSTRAT DESTINÉ À UN CIRCUIT ÉLECTRIQUE ET PROCÉDÉ DE FABRICATION DUDIT SUBSTRAT

Publication

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Application

**EP 16741600 A 20160718**

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Abstract (en)

[origin: WO2017013075A1] The invention relates to a substrate (1, 10) for electrical circuits, comprising at least one metal layer (2, 3, 14) and a paper ceramic layer (11), which is joined face to face with the at least one metal layer (2, 3, 14) and has a top side and bottom side (11a, 11b), wherein the paper ceramic layer (11) has a large number of cavities in the form of pores. Especially advantageously, the at least one metal layer (2, 3, 14) is connected to the paper ceramic layer (11) by means of at least one glue layer (6, 6a, 6b), which is produced by applying at least one glue (6a', 6a'', 6b', 6b'') to the metal layer (2, 3, 14) and/or to the paper ceramic layer (11), wherein the cavities in the form of pores in the paper ceramic layer (11) are filled at least at the surface by means of the applied glue (6a', 6a'', 6b', 6b'').

IPC 8 full level

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CPC (source: EP KR US)

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