

Title (en)

GENERATING A TIMEOUT SIGNAL BASED ON A CLOCK COUNTER ASSOCIATED WITH A DATA REQUEST

Title (de)

ERZEUGEN EINES ZEITÜBERSCHREITUNGSSIGNALS BASIEREND AUF EINEM MIT EINER DATENANFORDERUNG ASSOZIIERTEN TAKTZÄHLER

Title (fr)

GÉNÉRATION D'UN SIGNAL DE TEMPORISATION D'APRÈS UN COMPTEUR D'HORLOGE ASSOCIÉ À UNE DEMANDE DE DONNÉES

Publication

EP 3329379 A4 20190612 (EN)

Application

EP 15899843 A 20150729

Priority

US 2015042592 W 20150729

Abstract (en)

[origin: WO2017019052A1] Systems and methods for generating a timeout signal based on a clock counter associated with a data request. An interface component is configured for receiving a data request from a master device and forwarding the data request to a slave device. A timeout component is configured for maintaining a clock counter associated with the data request and generating a timeout signal in response to a determination that a threshold level associated with the clock counter is reached before receiving a data response associated with the data request from the slave device.

IPC 8 full level

G06F 13/14 (2006.01); **G06F 1/04** (2006.01); **G06F 11/07** (2006.01)

CPC (source: EP)

G06F 1/04 (2013.01); **G06F 11/0757** (2013.01); **G06F 13/14** (2013.01); **G06F 11/0736** (2013.01)

Citation (search report)

- [XY] US 2013262918 A1 20131003 - NATION GEORGE WAYNE [US], et al
- [Y] US 2010153602 A1 20100617 - KUME TAKAYUKI [JP], et al
- [Y] US 2013242749 A1 20130919 - HERZ KARL [DE], et al
- [Y] EP 2442231 A1 20120418 - ST MICROELECTRONICS GRENOBLE 2 [FR], et al
- See references of WO 2017019052A1

Designated contracting state (EPC)

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DOCDB simple family (application)

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