

Title (en)

DEBUGGING METHOD, MULTI-CORE PROCESSOR, AND DEBUGGING EQUIPMENT

Title (de)

FEHLERBESEITIGUNGSVERFAHREN, MEHRKERNPROZESSOR UND FEHLERBESEITIGUNGSAUSRÜSTUNG

Title (fr)

PROCÉDÉ DE DÉBOGAGE, PROCESSEUR MULTI-COEURS ET ÉQUIPEMENT DE DÉBOGAGE

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Application

EP 16848168 A 20160924

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Abstract (en)

[origin: EP3352083A1] Embodiments of the present invention relate to the field of computer technologies. The embodiments of the present invention provide a debugging method, including: stopping running, by a core A of the multi-core processor after completing execution of a processing routine of a preset event, and sending a running stop signal to other cores in a process of stopping running; after receiving a first stop termination instruction and resuming running, executing, by the core A, a debugging information collection function to collect debugging information of the preset event, and stopping running after completing the execution of the debugging information collection function; after receiving a second stop termination instruction and resuming running, sending, by the core A, a running resumption instruction to the other cores; and knocking, by the core A, the pending breakpoint in a process of running an operation object of the preset event, so as to enter a debugging state. According to the technical solutions provided in the embodiments of the present invention, kernel mode code and user mode code can be debugged on a same debugging platform. In addition, the embodiments of the present invention further provide a corresponding debugging apparatus.

IPC 8 full level

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Citation (search report)

- [A] US 2005216895 A1 20050929 - TRAN HIEU T [US]
- [A] US 2007266376 A1 20071115 - YIM KEUN-SOO [KR], et al
- See also references of WO 2017050287A1

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