

Title (en)

METHOD FOR SYNCHRONISED OPERATION OF MULTICORE PROCESSORS

Title (de)

VERFAHREN ZUM SYNCHRONISIERTEN BETRIEB VON MEHRKERNPROZESSOREN

Title (fr)

PROCÉDÉ DE FONCTIONNEMENT SYNCHRONISÉ DE PROCESSEURS MULTICOEURS

Publication

**EP 3449366 A1 20190306 (DE)**

Application

**EP 17733365 A 20170601**

Priority

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- EP 2017063260 W 20170601

Abstract (en)

[origin: WO2017220305A1] A method for synchronised operation of a plurality of multicore processors (MC1, MC2) is proposed, wherein a first (MC1) and a second (MC2) multicore processor each comprising a main processor core (A1, A2) and at least one secondary processor core (B1, B2, B3, B4) is provided that is used for executing utility programs. Only the main processor cores (A1, A2) of the various multicore processors (MC1, MC2) synchronise to one another. The at least one secondary processor core (B1, B2, B3, B4) is controlled by the respective main processor core (A1, A2) in each multicore processor (MC1, MC2). The utility programs are processed by the at least one secondary processor core (B1, B2, B3, B4) and outputs are generated that are made available to the respective main processor core (A1, A2) of the same multicore processor (MC1, MC2). Outputs from the multiplicity of multicore processors (MC1, MC2) are then output in sync by the respective main processor core (A1, A2).

IPC 8 full level

**G06F 9/50** (2006.01); **G06F 9/54** (2006.01); **G06F 11/16** (2006.01)

CPC (source: EP US)

**G06F 9/5066** (2013.01 - EP); **G06F 9/52** (2013.01 - US); **G06F 9/546** (2013.01 - EP); **G06F 11/1641** (2013.01 - EP); **G06F 15/82** (2013.01 - US); **G06F 11/1683** (2013.01 - EP)

Citation (search report)

See references of WO 2017220305A1

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