

Title (en)  
LOW-VOLTAGE MULTI-POLE ELECTRICAL BREAKER

Title (de)  
MEHRPOLIGER NIEDERSpannungSSCHALTER

Title (fr)  
DISJONCTEUR MULTIPOLAIRE À BASSE TENSION

Publication  
**EP 3489979 A1 20190529 (FR)**

Application  
**EP 18208111 A 20181123**

Priority  
FR 1761100 A 20171123

Abstract (en)  
[origin: US2019157028A1] A low-voltage multipolar circuit breaker includes a moulded housing including a main body that is divided into interior compartments, each associated with one pole of the circuit breaker, and a cover that is mounted on the main body covering a main face of the body. The circuit breaker includes, for each pole, electrical contacts that can be separated by a device of a switching mechanism and an arc-extinguishing chamber. For at least two of the poles, the main face includes an additional aperture, each placed directly above the spark guard of the corresponding pole and placing the arc-extinguishing chamber of this pole in communication with the interior volume delimited by the cover and the main face, and the cover is attached to the main body while leaving a peripheral opening between the cover and the main body, placing the interior volume in communication with the exterior of the housing.

Abstract (fr)  
Ce disjoncteur (2) multipolaire à basse tension comporte un boîtier moulé incluant un corps principal divisé en compartiments intérieurs chacun associé à un pôle (12, 14, 16) du disjoncteur et un capot monté sur le corps principal recouvrant une face principale (22) du corps. Le disjoncteur comporte, pour chaque pôle, des contacts électriques séparables par un mécanisme de commutation et une chambre de coupure. Pour au moins deux des pôles, la face principale comporte un orifice additionnel (18, 20) chacun placé à l'aplomb du pare étincelle du pôle correspondant et mettant en communication la chambre de coupure de ce pôle avec le volume intérieur délimité par le capot et la face principale, et le capot est fixé sur le corps principal en laissant une ouverture périphérique entre le capot et le corps principal mettant en communication le volume intérieur avec l'extérieur du boîtier.

IPC 8 full level  
**H01H 9/34** (2006.01)

CPC (source: EP US)  
**H01H 9/342** (2013.01 - EP US); **H01H 9/346** (2013.01 - EP US); **H01H 33/53** (2013.01 - US); **H01H 33/74** (2013.01 - US); **H01H 33/022** (2013.01 - US); **H01H 33/08** (2013.01 - US); **H01H 2009/348** (2013.01 - EP US); **H01H 2213/004** (2013.01 - US)

Citation (search report)

- [A] EP 0437151 A1 19910717 - MERLIN GERIN [FR]
- [A] WO 0203411 A1 20020110 - SIEMENS AG [DE], et al
- [A] EP 2469564 A1 20120627 - FUJII ELEC FA COMPONENTS & SYS [JP]

Designated contracting state (EPC)  
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)  
BA ME

DOCDB simple family (publication)  
**EP 3489979 A1 20190529**; **EP 3489979 B1 20200415**; CN 109830392 A 20190531; CN 109830392 B 20221104; ES 2787038 T3 20201014; FR 3073974 A1 20190524; FR 3073974 B1 20191220; US 10665403 B2 20200526; US 2019157028 A1 20190523

DOCDB simple family (application)  
**EP 18208111 A 20181123**; CN 201811390316 A 20181121; ES 18208111 T 20181123; FR 1761100 A 20171123; US 201816180312 A 20181105