

Title (en)
PROCESSING SYSTEM, RELATED INTEGRATED CIRCUIT AND METHOD

Title (de)
VERARBEITUNGSSYSTEM, ZUGEHÖRIGE INTEGRIERTE SCHALTUNG UND VERFAHREN

Title (fr)
SYSTÈME DE TRAITEMENT, CIRCUIT INTÉGRÉ ASSOCIÉ ET PROCÉDÉ

Publication
EP 3534262 A1 20190904 (EN)

Application
EP 19159273 A 20190226

Priority
IT 201800003233 A 20180302

Abstract (en)
A processing system is described. The processing system comprises a processing unit, such as a microprocessor, arranged to be connected to a memory with error detection and/or correction. Specifically, the processing unit generates at least one read request for reading data from the memory, the read request comprising an address signal () identifying the address of a given memory area in the memory. The processing system comprises moreover an error handling circuit (108) arranged to be connected to the memory for receiving an error signal () wherein the error signal () contains an error code indicating whether the data read from the memory contain errors (UC). Specifically, the error handling circuit (108) comprises a hardware circuit (1084, 1086) configured to set a first error signal () to the error code of the error signal () when the address indicated by the address signal () belongs to a first address range (,) and to set a second error signal () to the error code of the error signal () when the address indicated by the address signal () belongs to a second address range (,).

IPC 8 full level
G06F 11/10 (2006.01)

CPC (source: EP US)
G06F 11/0727 (2013.01 - US); **G06F 11/0751** (2013.01 - US); **G06F 11/0793** (2013.01 - US); **G06F 11/1048** (2013.01 - EP)

Citation (search report)
• [X] US 2010064205 A1 20100311 - MOYER WILLIAM C [US]
• [X] US 2011047408 A1 20110224 - GILLE DAMIEN RENE GILBERT [FR], et al

Cited by
EP4254196A1; EP4120083A1; US11764807B2

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)
BA ME

DOCDB simple family (publication)
EP 3534262 A1 20190904; **EP 3534262 B1 20201104**; IT 201800003233 A1 20190902; US 11068331 B2 20210720;
US 2019272211 A1 20190905

DOCDB simple family (application)
EP 19159273 A 20190226; IT 201800003233 A 20180302; US 201916289425 A 20190228