

Title (en)

AMPLIFIER CIRCUIT AND METHOD FOR OPERATING AN AMPLIFIER CIRCUIT

Title (de)

VERSTÄRKERSCHALTUNG UND VERFAHREN ZUM BETRIEB EINER VERSTÄRKERSCHALTUNG

Title (fr)

CIRCUIT AMPLIFICATEUR ET PROCÉDÉ POUR FAIRE FONCTIONNER UN CIRCUIT AMPLIFICATEUR

Publication

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Application

EP 17798123 A 20171026

Priority

- EP 16198640 A 20161114
- EP 2017077453 W 20171026

Abstract (en)

[origin: WO2018086899A1] The invention relates to an amplifier circuit (2a) functioning as a line driver and to a method for the operation thereof, wherein a DC/DC converter (1) is integrated in the amplifier circuit (2a) as a power stage.

IPC 8 full level

H03F 3/217 (2006.01); **H02M 3/139** (2006.01); **H03F 3/30** (2006.01); **H04B 3/54** (2006.01); **H04B 14/02** (2006.01); **H04L 12/10** (2006.01); **H04M 3/40** (2006.01)

CPC (source: EP US)

H02M 3/33584 (2013.01 - US); **H03F 3/2171** (2013.01 - EP US); **H03F 3/2173** (2013.01 - EP); **H04B 3/542** (2013.01 - EP); **H04B 3/548** (2013.01 - EP); **H04B 14/026** (2013.01 - EP US); **H04L 12/10** (2013.01 - EP); **H02M 3/33592** (2013.01 - EP); **H03F 2203/21161** (2013.01 - EP); **H04B 2203/5458** (2013.01 - EP); **Y02B 70/10** (2013.01 - EP)

Citation (search report)

See references of WO 2018086899A1

Cited by

CN115378368A

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

EP 3322092 A1 20180516; CN 109952704 A 20190628; CN 109952704 B 20230721; DE 202017007564 U1 20220921; EP 3539213 A1 20190918; EP 3958462 A1 20220223; US 11552603 B2 20230110; US 2021013848 A1 20210114; WO 2018086899 A1 20180517

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EP 16198640 A 20161114; CN 201780070355 A 20171026; DE 202017007564 U 20171026; EP 17798123 A 20171026; EP 2017077453 W 20171026; EP 21196742 A 20171026; US 201716349453 A 20171026