

Title (en)

INTEGRATED CIRCUIT CHIP DEVICE AND RELATED PRODUCT

Title (de)

CHIP-VORRICHTUNG MIT INTEGRIERTER SCHALTUNG UND ZUGEHÖRIGES PRODUKT

Title (fr)

DISPOSITIF À PUCE DE CIRCUIT INTÉGRÉ ET PRODUIT ASSOCIÉ

Publication

**EP 3624019 A1 20200318 (EN)**

Application

**EP 18894430 A 20181229**

Priority

- CN 201711499267 A 20171230
- CN 201711499268 A 20171230
- CN 201711499265 A 20171230
- CN 201711499266 A 20171230
- CN 2018125801 W 20181229

Abstract (en)

The present disclosure provides an integrated circuit chip device and a related product. The integrated circuit chip device includes: a primary processing circuit and a plurality of basic processing circuits. The primary processing circuit or at least one of the plurality of basic processing circuits includes a compression mapping circuit configured to perform compression on each data of a neural network operation. The technical solution provided by the present disclosure has the advantages of a small amount of computations and low power consumption.

IPC 8 full level

**G06N 3/063** (2006.01)

CPC (source: EP US)

**G06F 16/22** (2018.12 - US); **G06F 16/258** (2018.12 - US); **G06N 3/04** (2013.01 - US); **G06N 3/063** (2013.01 - EP US); **G06N 3/082** (2013.01 - US)

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

**EP 3624019 A1 20200318**; **EP 3624019 A4 20210324**; US 11704544 B2 20230718; US 2020175357 A1 20200604;  
WO 2019129302 A1 20190704

DOCDB simple family (application)

**EP 18894430 A 20181229**; CN 2018125801 W 20181229; US 201916698000 A 20191127