

Title (en)

INTERFERENCE MITIGATION SCHEMES FOR FULL DUPLEX CELLULAR SYSTEMS

Title (de)

INTERFERENZABSCHWÄCHUNGSSCHEMATA FÜR ZELLULARE VOLLDUPLEXSYSTEME

Title (fr)

SCHÉMAS D'ATTÉNUATION D'INTERFÉRENCE POUR DES SYSTÈMES CELLULAIRES EN DUPLEX INTÉGRAL

Publication

EP 3682564 A4 20210421 (EN)

Application

EP 17924431 A 20170911

Priority

US 2017050977 W 20170911

Abstract (en)

[origin: WO2019050544A1] Devices and methods for interference mitigation in a full duplex cellular system are generally described. An eNB can include processing circuitry configured to, in response to a request for interference information, determine an UL interference indicator. The UL interference indicator quantifies eNB-to-eNB interference resulting from DL traffic sent from one or more neighboring eNBs and UE-to-eNB interference resulting from UL traffic sent to the eNB, the DL traffic and the UL traffic using overlapping time and frequency resources. The processing circuitry can further decode a parameter update request, which includes a power boosting factor. The power boosting factor is selected based on a ratio of the UE-to-eNB interference and the eNB-to-eNB interference. The power boosting factor is encoded for transmission to one or more UEs within a cell of the eNB, the power boosting factor indicating a power level adjustment in the transmit power of the one or more UEs.

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

- [X] WO 2017111807 A1 20170629 - INTEL CORP [US]
- [A] US 2014177486 A1 20140626 - WANG YIPING [US], et al
- [A] "GROUP SPECIFICATION", no. ; 20170501, 7 June 2017 (2017-06-07), XP051283873, Retrieved from the Internet <URL:http://www.3gpp.org/ftp/Meetings_3GPP_SYNC/SA/Docs/> [retrieved on 20170607]
- See references of WO 2019050544A1

Designated contracting state (EPC)

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