

Title (en)

DRIVE COMPENSATION CIRCUIT AND DATA DRIVE APPARATUS

Title (de)

LAUFWERKKOMPENSATIONSSCHALTUNG UND DATENLAUFWERK

Title (fr)

CIRCUIT DE COMPENSATION DE PILOTAGE ET APPAREIL DE PILOTAGE DE DONNÉES

Publication

**EP 3703043 A4 20210728 (EN)**

Application

**EP 17929435 A 20171116**

Priority

- CN 201711009010 A 20171025
- CN 2017111259 W 20171116

Abstract (en)

[origin: EP3703043A1] Disclosed in the present invention is a drive compensation circuit, comprising: a first temporary storage unit, a second temporary storage unit, a gating unit, an electric displacement converter, a digital-to-analog conversion unit and an amplification unit; the first temporary storage unit and the second temporary storage unit are respectively connected to the gating unit, while the gating unit, the electric displacement converter, the digital-to-analog conversion unit and the amplification unit are connected in sequence.

IPC 8 full level

**G09G 3/20** (2006.01); **G09G 3/3233** (2016.01); **G09G 3/3275** (2016.01)

CPC (source: CN EP KR)

**G09G 3/20** (2013.01 - CN EP KR); **G09G 3/3233** (2013.01 - EP); **G09G 3/3275** (2013.01 - EP); **G09G 2310/0264** (2013.01 - CN EP); **G09G 2310/0289** (2013.01 - KR)

Citation (search report)

- [X] CN 107039003 A 20170811 - SHENZHEN CHINA STAR OPTOELECT
- [A] US 2008297500 A1 20081204 - KATO HIROFUMI [JP]
- [A] US 2017140720 A1 20170518 - GUO CHUNPENG [CN], et al
- See references of WO 2019080211A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

**EP 3703043 A1 20200902**; **EP 3703043 A4 20210728**; CN 107845359 A 20180327; JP 2020531915 A 20201105; KR 102365389 B1 20220218; KR 20200063245 A 20200604; WO 2019080211 A1 20190502

DOCDB simple family (application)

**EP 17929435 A 20171116**; CN 201711009010 A 20171025; CN 2017111259 W 20171116; JP 2020511500 A 20171116; KR 20207014543 A 20171116