

Title (en)

RIPPLE SUPPRESSION CIRCUIT, CONTROLLING METHOD AND DRIVING EQUIPMENT

Title (de)

SCHALTUNG ZUR UNTERDRÜCKUNG VON WELLIGKEIT, STEUERVERFAHREN UND ANTRIEBSVORRICHTUNG

Title (fr)

CIRCUIT DE SUPPRESSION D'ONDULATIONS, PROCÉDÉ DE COMMANDE ET ÉQUIPEMENT D'ACTIVATION

Publication

EP 3741190 A1 20201125 (EN)

Application

EP 18907985 A 20180228

Priority

CN 2018077508 W 20180228

Abstract (en)

[origin: WO2019165589A1] A ripple suppression circuit, a controlling method and a driving equipment. The ripple suppression circuit includes: a first input port and a second input port, configured to receive an input signal; a first output port and a second output port, configured to output an output signal, the second input port connects with the second output port; at least one capacitor, configured to connect between the first input port and the second input port; a main transistor, configured to connect between the first input port and the first output port, and suppress ripples in the input signal; and a protection circuit, configured to connect between the first input port and the first output port, the protection circuit connects the first input port with the first output port when there is a short circuit between the first output port and the second output port. Therefore, the main transistor can be protected.

IPC 8 full level

H05B 45/50 (2022.01); **H05B 45/59** (2022.01)

CPC (source: EP US)

H05B 45/36 (2020.01 - EP US); **H05B 45/3725** (2020.01 - EP US); **H05B 45/50** (2020.01 - EP US); **H05B 45/59** (2022.01 - EP US); **H05B 47/10** (2020.01 - EP US); **H05B 47/20** (2020.01 - EP US); **Y02B 20/30** (2013.01 - EP)

Designated contracting state (EPC)

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Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

WO 2019165589 A1 20190906; CN 111742618 A 20201002; EP 3741190 A1 20201125; EP 3741190 A4 20210901

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