

Title (en)
SECURE DATA PROCESSING

Title (de)
SICHERE DATENVERARBEITUNG

Title (fr)
TRAITEMENT SÉCURISÉ DE DONNÉES

Publication
EP 3765984 A1 20210120 (FR)

Application
EP 19714232 A 20190313

Priority
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Abstract (en)
[origin: WO2019175482A1] Devices and methods for executing instructions in an automatic and secure manner. A security processor (PS) comprising at least a read-only memory (ROM), a random access memory (RAM), a computer (UE) capable of performing cryptographic functions, a monotonic counter management unit (UG) associated with one or more monotonic counters (CM), is such that it does not comprise any other storage memory, meaning that the security processor (PS) does not store any program or external data, a public key (CP) allowing at least one initial enrolled administrator (AI) to be authenticated is stored before the first use of same in its read-only memory (ROM), its random access memory (RAM) is capable of loading a set of data and instructions that can be authenticated by a public key cryptographic module (MC), the execution by the computer (UE), after the authentication of same, of certain instructions, increments one of the monotonic counters (CM), so as to be able to automatically execute a sequence of operations in a secure manner, such a security processor (PS) being included in an assembly or infrastructure for secure data processing.

IPC 8 full level
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CPC (source: EP KR US)
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