

Title (en)

COUPON AND METHOD FOR CHECKING A PRINTED CIRCUIT BOARD

Title (de)

TESTCOUPON UND VERFAHREN ZUR ÜBERPRÜFUNG EINER LEITERPLATTE

Title (fr)

ÉPROUVETTE ET PROCÉDÉ DE VÉRIFICATION D'UNE CARTE DE CIRCUITS IMPRIMÉS

Publication

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Application

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Priority

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Abstract (en)

[origin: WO2019179984A1] The invention relates to a coupon (110) for a panel (100) with at least one printed circuit board (101). The printed circuit board (101) has M layers (121), where M is greater than or equal to 2, said layers being electrically insulated from one another by a respective substrate (122, 123). The coupon (110) comprises M test layers (241) for the corresponding M layers (121) of the printed circuit board (101). Each of the M test layers (241) has an electrically conductive reference surface (212) and a test line (211, 213) which is electrically insulated from the reference surface. The reference surface (212) of one test layer (241) is designed as a reference layer for the test line (212, 213) of a directly adjacent test layer (241).

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

See references of WO 2019179984A1

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