

Title (en)

SAMPLING APPARATUS FOR DETECTING A STARTING BYTE IN A HIGH-FREQUENCY SERIAL DATA STREAM

Title (de)

ABTASTVORRICHTUNG ZUR DETEKTION EINES START-BYTES IN EINEM HOCHFREQUENTEN SERIELLEN DATENSTROM

Title (fr)

DISPOSITIF DE BALAYAGE POUR LA DÉTECTION D'UN BIT DE DÉPART DANS UN FLUX DE DONNÉES SÉRIE HAUTE FRÉQUENCE

Publication

**EP 3821571 A1 20210519 (DE)**

Application

**EP 18742744 A 20180711**

Priority

EP 2018068800 W 20180711

Abstract (en)

[origin: WO2020011347A1] A sampling apparatus (10) for detecting a starting byte in a high-frequency serial data stream (D), having a detection unit (16), which is designed to detect the starting byte in the data stream (D), and a clock signal generator (18), which is designed to provide the detection unit (16) with a high-frequency clock signal (T1) synchronised to the data stream (D), wherein the clock signal (T1) has a higher frequency than the data stream (D), wherein the detection unit (16) is formed by a simple logical detection circuit (19) that excludes addition blocks.

IPC 8 full level

**H04L 12/40** (2006.01); **G05B 19/414** (2006.01); **G06F 13/42** (2006.01); **H04L 5/14** (2006.01); **H04L 27/156** (2006.01); **H04L 27/26** (2006.01)

CPC (source: EP US)

**H04L 7/0087** (2013.01 - US); **H04L 12/40013** (2013.01 - EP); **H04L 5/1484** (2013.01 - EP)

Citation (search report)

See references of WO 2020011347A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

**WO 2020011347 A1 20200116**; CN 112385184 A 20210219; CN 112385184 B 20220614; EP 3821571 A1 20210519; JP 2021524700 A 20210913; US 2021281387 A1 20210909

DOCDB simple family (application)

**EP 2018068800 W 20180711**; CN 201880094712 A 20180711; EP 18742744 A 20180711; JP 2021500522 A 20180711; US 201817258450 A 20180711