

Title (en)
SHIFT-REGISTER UNIT CIRCUIT, GATE-DRIVING CIRCUIT, DISPLAY APPARATUS, AND DRIVING METHOD

Title (de)
SCHALTUNG FÜR SCHIEBEREGISTEREINHEIT, GATE-TREIBERSCHALTUNG, ANZEIGEVORRICHTUNG UND ANSTEUERVERFAHREN

Title (fr)
CIRCUIT D'UNITÉ DE REGISTRE À DÉCALAGE, CIRCUIT D'EXCITATION DE PORTE, APPAREIL D'AFFICHAGE, ET PROCÉDÉ D'EXCITATION

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EP 3830817 A1 20210609 (EN)

Application
EP 19839244 A 20190628

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Abstract (en)
[origin: WO2020019936A1] A shift-register unit circuit (100) includes a first input sub-circuit (120) configured to receive a display-input signal from a display-input terminal (STU2, VDD,VGH) and input a display output-control signal to a first node (Q) based on the display-input signal during a display period of one cycle of displaying one frame of image. The shift-register unit circuit (100) also includes a second input sub-circuit (110) configured to receive a blank-input signal for charging a blank-control node (H), and configured to input a blank-output-control signal to the first node (Q) based on the blank-input signal during a blank period of the one cycle. The shift-register unit circuit (100) further includes an output sub-circuit (130) configured to output a hybrid signal controlled by the first node (Q). The second input sub-circuit (110) is also configured, before an end of the blank period, to receive a first blank-reset signal to reset the blank-control node (H).

IPC 8 full level
G09G 3/3208 (2016.01)

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