

Title (en)

ELECTRONIC CIRCUIT ARRANGEMENT AND METHOD FOR CONTINUOUSLY CHECKING THE ELECTRICAL CONTINUITY OF A CONDUCTOR WIRE, IN PARTICULAR A WIRE CONNECTING A DEVICE EMITTING A DIGITAL ELECTRICAL SIGNAL AND A REMOTE PROCESSING UNIT OF SAID SIGNAL

Title (de)

ELEKTRONISCHE SCHALTUNGSANORDNUNG UND VERFAHREN ZUR KONTINUIERLICHEN ÜBERPRÜFUNG DER ELEKTRISCHEN KONTINUITÄT EINES LEITERDRAHTES, INSBESONDERE EINES DRAHTES, DER EINE EIN DIGITALES ELEKTRISCHES SIGNAL AUSSENDENDE VORRICHTUNG UND EINE ENTFERNT VERARBEITUNGSEINHEIT DIESES SIGNALS VERBINDET

Title (fr)

AGENCEMENT DE CIRCUITS ÉLECTRONIQUES ET PROCÉDÉ POUR VÉRIFIER EN CONTINU LA CONTINUITÉ ÉLECTRIQUE D'UN FIL CONDUCTEUR, DANS UN FIL PARTICULIER CONNECTANT UN DISPOSITIF ÉMETTANT UN SIGNAL ÉLECTRIQUE NUMÉRIQUE ET UNE UNITÉ DE TRAITEMENT À DISTANCE DUDIT SIGNAL

Publication

**EP 3841563 A1 20210630 (EN)**

Application

**EP 19773546 A 20190823**

Priority

- IT 201800008166 A 20180823
- IB 2019057108 W 20190823

Abstract (en)

[origin: WO2020039395A1] An electronic circuit arrangement and a method are described for the remote transmission via wire of a digital signal emitted by an emitting device of an electrical state signal, associated with a plant in the field, to a remote processing unit of said digital signal. At the transmission end of a wired connection between the emitting device and a remote processing unit, a composite signal for transmission via wire is generated, comprising a signal portion representative of the logical state of the digital signal emitted by the emitting device and a signal portion for determining the electrical continuity of the transmission link. At a receiving end of the wired connection, a signal recognition electronic circuit that may be associated with the remote processing unit is arranged to receive the composite signal and to verify the existence of at least one change in the logical state of the composite signal within a time interval corresponding to a signaling period. The existence of at least one change in the logical state of the composite signal within a time interval corresponding to a signaling period is indicative of the integrity of the wired connection.

IPC 8 full level

**G08C 19/00** (2006.01)

CPC (source: EP)

**G08C 19/00** (2013.01); **G08C 25/02** (2013.01)

Citation (search report)

See references of WO 2020039395A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

**WO 2020039395 A1 20200227**; **WO 2020039395 A8 20210218**; EP 3841563 A1 20210630; EP 3841563 B1 20230927; EP 3841563 C0 20230927; IT 201800008166 A1 20200223; MA 53441 A 20210630

DOCDB simple family (application)

**IB 2019057108 W 20190823**; EP 19773546 A 20190823; IT 201800008166 A 20180823; MA 53441 A 20190823