Title (en)

HARDWARE TRANSMIT EQUILIZATION FOR HIGH SPEED

Title (de)

HARDWARE-ÜBERTRAGUNGSAUSGLEICH FÜR HOHE GESCHWINDIGKEIT

Title (fr)

ÉQUILIBRAGE DE MATÉRIEL DE TRANSMISSION POUR GRANDE VITESSE

Publication

EP 3850494 B1 20220706 (EN)

Application

EP 19773310 A 20190910

Priority

- US 201816130791 A 20180913
- US 2019050304 W 20190910

Abstract (en)

[origin: US10541841B1] Systems, apparatuses, and methods for performing transmit equalization at a target high speed are disclosed. A computing system includes at least a transmitter, receiver, and a communication channel connecting the transmitter and the receiver. The communication channel includes a plurality of lanes which are subdivided into a first subset of lanes and a second subset of lanes. During equalization training, the first subset of lanes operate at a first speed while the second subset of lanes operate at a second speed. The first speed is the desired target speed for operating the communication link while the second speed is a relatively low speed capable of reliably carrying data over a given lane prior to equalization training. The first subset of lanes are trained at the first speed while feedback is conveyed from the receiver to the transmitter using the second subset of lanes operating at the second speed.

IPC 8 full level

G06F 13/42 (2006.01)

CPC (source: EP KR US)

G06F 13/4278 (2013.01 - EP KR); **H04L 7/0008** (2013.01 - KR US); **H04L 25/0272** (2013.01 - KR US); **H04L 25/03878** (2013.01 - KR US)

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