

Title (en)

INTEGRATED CIRCUIT AND STANDARD CELL THEREOF

Title (de)

INTEGRIERTE SCHALTUNG UND STANDARDZELLE DAFÜR

Title (fr)

CIRCUIT INTÉGRÉ ET CELLULE STANDARD ASSOCIÉE

Publication

**EP 3867951 A1 20210825 (EN)**

Application

**EP 18799753 A 20181107**

Priority

EP 2018080402 W 20181107

Abstract (en)

[origin: WO2020094220A1] The present invention relates to an integrated circuit, particularly a CMOS integrated circuit, which includes at least one diffusion layer. The diffusion layer may especially be a continuous diffusion layer. The present invention provides accordingly an integrated circuit including: at least one power rail, the at least one diffusion layer, at least one isolation gate provided on the diffusion layer and configured to electrically isolate a first region of the diffusion layer from a second region of the diffusion layer, and at least one gate-via vertically connecting the isolation gate to the power rail.

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

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