

Title (en)

PROCESS FLOW FOR HYBRID TFT-BASED MICRO DISPLAY PROJECTOR

Title (de)

PROZESSABLAUF FÜR AUF HYBRIDEN TFT BASIERENDEN MIKROANZEIGEPROJEKTOR

Title (fr)

FLUX DE TRAITEMENT POUR PROJECTEUR À MICRO-ÉCRAN D’AFFICHAGE À BASE DE TFT HYBRIDE

Publication

EP 3877804 A1 20210915 (EN)

Application

EP 20713122 A 20200205

Priority

- US 201962801424 P 20190205
- US 201962861254 P 20190613
- US 201962863659 P 20190619
- US 201916660648 A 20191022
- US 201916660643 A 20191022
- US 201962924604 P 20191022
- US 202016780486 A 20200203
- US 2020016727 W 20200205

Abstract (en)

[origin: TW202101063A] Disclosed herein are techniques for forming a thin-film circuit layer on an array of light-emitting diodes (LEDs). LEDs in the array of LEDs can be singulated by various processes, such as etching and ion implantation. Singulating LEDs can be performed before or after forming the thin-film circuit layer on the array of LEDs. The array of LEDs can be bonded to a transparent or non-transparent substrate.

IPC 8 full level

G02B 27/01 (2006.01); **H01L 27/06** (2006.01); **H01L 27/12** (2006.01); **H01L 27/146** (2006.01)

CPC (source: EP KR)

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Citation (search report)

See references of WO 2020163436A1

Cited by

US11355665B2; US11699773B2

Designated contracting state (EPC)

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Designated extension state (EPC)

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