

Title (en)

METHOD AND DEVICE FOR OPERATING A MEMORY ASSEMBLY

Title (de)

VERFAHREN UND VORRICHTUNG ZUM BETREIBEN EINER SPEICHERANORDNUNG

Title (fr)

PROCÉDÉ ET DISPOSITIF POUR LE FONCTIONNEMENT D'UN ENSEMBLE MÉMOIRE

Publication

EP 3884490 A1 20210929 (DE)

Application

EP 19782986 A 20191001

Priority

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- EP 2019076585 W 20191001

Abstract (en)

[origin: WO2020104091A1] The invention relates to a method for operating a memory assembly. A physical address is received. The physical address is associated with a first memory segment of a memory assembly. The physical address is modified to a modified physical address. The modified physical address is associated with a second memory segment of the memory assembly.

IPC 8 full level

G11C 8/10 (2006.01)

CPC (source: EP US)

G06F 7/588 (2013.01 - US); **G06F 12/0246** (2013.01 - EP); **G11C 7/1012** (2013.01 - US); **G11C 7/1039** (2013.01 - US); **G11C 7/20** (2013.01 - US); **G11C 8/10** (2013.01 - EP); **G11C 29/76** (2013.01 - EP); **H03K 19/21** (2013.01 - US); **G06F 2212/1024** (2013.01 - EP); **G06F 2212/1036** (2013.01 - EP); **G06F 2212/7204** (2013.01 - EP); **G06F 2212/7211** (2013.01 - EP)

Citation (search report)

See references of WO 2020104091A1

Designated contracting state (EPC)

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Designated extension state (EPC)

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DE 102018128980 A1 20200520; CN 112912957 A 20210604; EP 3884490 A1 20210929; US 11557327 B2 20230117; US 2021398571 A1 20211223; WO 2020104091 A1 20200528

DOCDB simple family (application)

DE 102018128980 A 20181119; CN 201980069238 A 20191001; EP 19782986 A 20191001; EP 2019076585 W 20191001; US 201917292145 A 20191001