

Title (en)
ANALOGUE VOLTAGE PROGRAMMING

Title (de)
ANALOGSPANNUNGSPROGRAMMIERUNG

Title (fr)
PROGRAMMATION DE TENSION ANALOGUE

Publication
EP 3888212 A1 20211006 (DE)

Application
EP 20726088 A 20200514

Priority
• DE 102019114228 A 20190528
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Abstract (en)
[origin: WO2020239454A1] The invention relates to an analogue circuit arrangement (1) for variably setting a voltage U_{out} within defined voltage limits, comprising a non-inverting adder (10) having a positive input (11), wherein a voltage divider (20) comprising at least a first stage (21) and a second stage (22) is connected to the positive input (11) of the adder (10), wherein at least one stage comprises a parallel circuit of n resistors (R_1, R_2, \dots, R_n) which are each connected in series in a conduction path (L_1, L_2, \dots, L_n) to an overcurrent protection device (F_1, F_2, \dots, F_n), and at least one device (30) for actively changing one or more of the overcurrent protection devices (F_1, F_2, \dots, F_n) into a state which interrupts the respective affected conduction path (L_1, L_2, \dots, L_n).

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Citation (search report)
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WO 2020239454 A1 20201203; CN 211506890 U 20200915; DE 102019114228 A1 20201203; EP 3888212 A1 20211006; KR 102672303 B1 20240604; KR 20220011610 A 20220128; US 11977400 B2 20240507; US 2022187860 A1 20220616

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