

Title (en)
DEVICE FOR GENERATING ANALOGUE SIGNALS

Title (de)
VORRICHTUNG ZUR ERZEUGUNG VON ANALOGEN SIGNALEN

Title (fr)
DISPOSITIF DE GENERATION DE SIGNAUX ANALOGIQUES

Publication
EP 3888248 A1 20211006 (FR)

Application
EP 19805282 A 20191118

Priority
• FR 1872049 A 20181129
• EP 2019081587 W 20191118

Abstract (en)
[origin: WO2020109041A1] Device for generating analogue signals, comprising a digital-to-analogue converter comprising at least one digital input and one analogue output, a circuit for generating a first clock signal of frequency f_s , and a digital register configured so as to receive at the input and to store N bits representative of an analogue output signal of the converter, N being an integer greater than or equal to 1, and for receiving the first clock signal, the register comprising, for each bit, two complementary digital outputs, characterised in that it comprises a circuit for generating a second clock signal, of frequency $m \times f_s$, where m is an integer greater than 1, and N multiplexer circuits, placed between the digital register outputs and the inputs of the converter and configured so as to each receive, on a control input, the second clock signal and to each receive, on a data input, signals originating from the two digital outputs of the register corresponding to the same input bit of the register, such that the frequency of the signals leaving the multiplexor circuits is $2 \times m \times f_s$.

IPC 8 full level
H03M 1/66 (2006.01)

CPC (source: EP KR US)
H03M 1/66 (2013.01 - EP KR US); **H03M 1/662** (2013.01 - US); **H03M 1/802** (2013.01 - KR US)

Citation (search report)
See references of WO 2020109041A1

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