

Title (en)

METHODS, APPARATUS, AND SYSTEMS TO FACILITATE A FAULT TRIGGERED DIODE EMULATION MODE OF A TRANSISTOR

Title (de)

VERFAHREN, VORRICHTUNG UND SYSTEME FÜR FEHLERTOLERANTEN DIODENEMULATIONSMODUS EINES TRANSISTORS

Title (fr)

PROCÉDÉS, APPAREIL ET SYSTÈMES POUR FACILITER UN MODE D'ÉMULATION DE DIODE DÉCLENCHÉ EN CAS DE DÉFAUT D'UN TRANSISTOR

Publication

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Application

EP 20778288 A 20200326

Priority

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Abstract (en)

[origin: US2020313421A1] Methods, systems, and apparatus to facilitate a fault triggered diode emulation mode of a transistor. An example apparatus includes a driver to output a control signal to a gate terminal of a transistor of a power converter; and a diode emulation control circuit to, in response to determining a fault corresponding to the transistor, enable the transistor when current flows in a direction from a source terminal of the transistor to a drain terminal of the transistor.

IPC 8 full level

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CPC (source: EP US)

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Citation (search report)

- [XAI] US 2011254526 A1 20111020 - LUO SHIGUO [US], et al
- [XAI] US 2017187283 A1 20170629 - VAIDYA VAIBHAV [US], et al
- See also references of WO 2020198410A1

Designated contracting state (EPC)

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US 11183832 B2 20211123; **US 2020313421 A1 20201001**; EP 3966656 A1 20220316; EP 3966656 A4 20220824; US 11621551 B2 20230404; US 2022037873 A1 20220203; WO 2020198410 A1 20201001; WO 2020198410 A8 20210415

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