

Title (en)

DEVICE, SYSTEM, AND METHOD TO OPTIMIZE BATTERY TEMPERATURE IN AN ELECTRONIC DEVICE

Title (de)

VORRICHTUNG, SYSTEM UND VERFAHREN ZUR OPTIMIERUNG DER BATTERIETEMPERATUR IN EINER ELEKTRONISCHEN VORRICHTUNG

Title (fr)

DISPOSITIF, SYSTÈME ET PROCÉDÉ DESTINÉS À OPTIMISER LA TEMPÉRATURE DE BATTERIE DANS UN DISPOSITIF ÉLECTRONIQUE

Publication

EP 3973591 A1 20220330 (EN)

Application

EP 20757736 A 20200730

Priority

US 2020044243 W 20200730

Abstract (en)

[origin: WO2022025897A1] An electronic device assembly can include an outer housing, a battery, an insulation layer, a printed circuit board, and an elongated heat conductor. The battery and insulation layer may be disposed within the outer housing. The printed circuit board may be positioned adjacent the insulation layer. The heat conductor may be directly adjacent the insulation layer and extend between the insulation layer and the printed circuit board. The insulation layer may extend between an interior surface of the outer housing and a surface of the elongated heat conductor.

IPC 8 full level

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CPC (source: CN EP)

H01M 10/615 (2015.04 - CN EP); **H01M 10/623** (2015.04 - CN); **H01M 10/635** (2015.04 - CN); **H01M 10/655** (2015.04 - CN); **H01M 10/657** (2015.04 - CN); **H01M 50/202** (2021.01 - CN EP); **H01M 50/247** (2021.01 - CN EP); **H01M 2220/30** (2013.01 - CN EP); **Y02E 60/10** (2013.01 - EP)

Citation (search report)

See references of WO 2022025897A1

Designated contracting state (EPC)

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