Title (en)

UPDATING FIRMWARE IN A CHIPSET OF A PERIPHERAL DEVICE

Title (de)

AKTUALISIERUNG VON FIRMWARE IN EINEM CHIPSATZ EINER PERIPHERIEVORRICHTUNG

Title (fr)

MISE À JOUR D'UN MICROLOGICIEL DANS UN JEU DE PUCES D'UN DISPOSITIF PÉRIPHÉRIQUE

Publication

EP 4004713 A1 20220601 (EN)

Application

EP 19790775 A 20190731

Priority

IB 2019000898 W 20190731

Abstract (en)

[origin: WO2021019270A1] Apparatus and method (200) for updating firmware in a chipset having a chipset microcontroller and a chipset memory. The chipset microcontroller is placed in an inactive state (208), such as during a boot reinitialization sequence. A host microcontroller writes a firmware image, representing updated firmware for the 5 chipset microcontroller, directly to the chipset memory (210) using an SPI (Serial Peripheral Interface) connection. The host microcontroller reads back the transferred firmware image from the chipset memory to verify a successful transfer of the firmware image (212). The chipset microcontroller is subsequently transitioned to an active state to execute the transferred firmware image (214). The chipset may form a 10 controller of a computer peripheral device, such as a data storage device. The firmware image may be supplied to the chipset memory using an HID (Human Interface Device).

IPC 8 full level

G06F 8/654 (2018.01); **G06F 11/14** (2006.01); **G06F 21/57** (2013.01)

CPC (source: EP)

G06F 8/654 (2018.01); **G06F 21/572** (2013.01); G06F 11/1433 (2013.01); G06F 21/575 (2013.01)

Citation (search report)

See references of WO 2021019270A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

WO 2021019270 A1 20210204; EP 4004713 A1 20220601

DOCDB simple family (application)

IB 2019000898 W 20190731; EP 19790775 A 20190731