

Title (en)

CLOCK OSCILLATOR AND METHOD FOR PREPARING CLOCK OSCILLATOR

Title (de)

TAKTOSZILLATOR UND VERFAHREN ZUR HERSTELLUNG EINES TAKTOSZILLATORS

Title (fr)

OSCILLATEUR D'HORLOGE ET PROCÉDÉ DE PRÉPARATION D'OSCILLATEUR D'HORLOGE

Publication

EP 4007161 A1 20220601 (EN)

Application

EP 21209574 A 20211122

Priority

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- CN 202110183763 A 20210210

Abstract (en)

A clock oscillator, a method for preparing the clock oscillator, a method for using the clock oscillator, and a chip that includes the clock oscillator are provided. The clock oscillator includes a first resonator, a second resonator, and a frequency synthesis module, where an output frequency of the first resonator is higher than an output frequency of the second resonator, the frequency synthesis module is configured to generate a synthesis frequency based on the output frequency of the first resonator and the output frequency of the second resonator, and the synthesis frequency is used as a clock frequency output by the clock oscillator. The clock oscillator uses both of the two resonators with the different output frequencies as clock signal sources, and generates a synthesized clock signal by using the frequency synthesis module. In this way, one clock oscillator is used to meet requirements of a plurality of ICT clock application scenarios, thereby reducing device complexity and production costs.

IPC 8 full level

H03B 5/32 (2006.01); **H03B 21/01** (2006.01); **H03L 7/06** (2006.01)

CPC (source: BR EP US)

G06F 1/08 (2013.01 - US); **H03B 5/32** (2013.01 - EP); **H03B 5/36** (2013.01 - BR); **H03B 5/38** (2013.01 - BR); **H03H 9/02102** (2013.01 - US); **H03H 9/02448** (2013.01 - US); **H03L 1/04** (2013.01 - EP); **H03L 7/0812** (2013.01 - EP); **H03L 7/089** (2013.01 - US); **H03L 7/0992** (2013.01 - US); **H03B 5/326** (2013.01 - BR); **H03B 2200/0018** (2013.01 - EP)

Citation (search report)

- [XYI] US 2019035848 A1 20190131 - TANAKA ATSUSHI [JP], et al
- [XYI] US 2020235701 A1 20200723 - NOMURA NORIO [JP]
- [Y] CN 107785278 A 20180309 - SHANGHAI MICROLINK SENSTECH CO LTD
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Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

DOCDB simple family (publication)

EP 4007161 A1 20220601; BR 102021023744 A2 20220614; JP 2022087073 A 20220609; JP 7410108 B2 20240109; US 2022171426 A1 20220602

DOCDB simple family (application)

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