Title (en)
   MEMORY-BASED PROCESSORS

Title (de)
   SPEICHERBASIERTE PROZESSOREN

Title (fr)
   PROCESSEURS À BASE DE MÉMOIRE

Publication
   EP 4010808 A2 20220615 (EN)

Application
   EP 20852497 A 20200813

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Abstract (en)
   [origin: WO2021028723A2] In some embodiments, an integrated circuit may include a substrate and a memory array disposed on the substrate,
   where the memory array includes a plurality of discrete memory banks. The integrated circuit may also include a processing array disposed on the
   substrate, where the processing array includes a plurality of processor subunits, each one of the plurality of processor subunits being associated
   with one or more discrete memory banks among the plurality of discrete memory banks. The integrated circuit may also include a controller
   configured to implement at least one security measure with respect to an operation of the integrated circuit and take one or more remedial actions if
   the at least one security measure is triggered.

IPC 8 full level
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