

Title (en)
MEMORY-BASED PROCESSORS

Title (de)
SPEICHERBASIERTE PROZESSOREN

Title (fr)
PROCESSEURS À BASE DE MÉMOIRE

Publication
EP 4010808 A4 20231115 (EN)

Application
EP 20852497 A 20200813

Priority

- US 201962886328 P 20190813
- US 201962907659 P 20190929
- US 201962930593 P 20191105
- US 202062971912 P 20200207
- US 202062983174 P 20200228
- IB 2020000665 W 20200813

Abstract (en)
[origin: WO2021028723A2] In some embodiments, an integrated circuit may include a substrate and a memory array disposed on the substrate, where the memory array includes a plurality of discrete memory banks. The integrated circuit may also include a processing array disposed on the substrate, where the processing array includes a plurality of processor subunits, each one of the plurality of processor subunits being associated with one or more discrete memory banks among the plurality of discrete memory banks. The integrated circuit may also include a controller configured to implement at least one security measure with respect to an operation of the integrated circuit and take one or more remedial actions if the at least one security measure is triggered.

IPC 8 full level
G06F 12/14 (2006.01); **G06F 12/02** (2006.01); **G06F 15/78** (2006.01); **G11C 7/10** (2006.01)

CPC (source: EP KR)
G06F 12/0215 (2013.01 - EP); **G06F 12/1441** (2013.01 - EP); **G06F 15/7821** (2013.01 - EP KR); **G06F 21/78** (2013.01 - EP);
G11C 7/1006 (2013.01 - EP KR); **G06F 12/0811** (2013.01 - EP); **G06F 12/0813** (2013.01 - EP); **G06F 12/0862** (2013.01 - EP);
G06F 2212/454 (2013.01 - EP); **Y02D 10/00** (2017.12 - EP KR)

Citation (search report)

- [X] US 2012005421 A1 20120105 - MIURA SEIJI [JP], et al
- [X] STANKOVIC V V ET AL: "DRAM Controller with a Complete Predictor: Preliminary Results", TELECOMMUNICATIONS IN MODERN SATELLITE, CABLE AND BROADCASTING SERVICE S, 2005. 7TH INTERNATIONAL CONFERENCE ON NIS, SERBIA AND MONTENEGRO 28-30 SEPT. 2005, PISCATAWAY, NJ, USA, IEEE, vol. 2, 28 September 2005 (2005-09-28), pages 593 - 596, XP010874696, ISBN: 978-0-7803-9164-2, DOI: 10.1109/TELSKS.2005.1572183
- See references of WO 2021028723A2

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)
WO 2021028723 A2 20210218; WO 2021028723 A3 20210708; CN 114586019 A 20220603; EP 4010808 A2 20220615;
EP 4010808 A4 20231115; KR 20220078566 A 20220610; TW 202122993 A 20210616

DOCDB simple family (application)
IB 2020000665 W 20200813; CN 202080071415 A 20200813; EP 20852497 A 20200813; KR 20227008116 A 20200813;
TW 109127495 A 20200813