

Title (en)
PALETTE AND PREDICTION MODE SIGNALING

Title (de)
PALETTE UND SIGNALISIERUNG DES VORHERSAGEMODUS

Title (fr)
SIGNALISATION DE MODE DE PRÉDICTION ET DE MODE PALETTE

Publication
EP 4011075 A1 20220615 (EN)

Application
EP 20757484 A 20200804

Priority

- US 201962883024 P 20190805
- US 201962887450 P 20190815
- US 201962900346 P 20190913
- US 202016947463 A 20200803
- US 2020044817 W 20200804

Abstract (en)
[origin: US2021044812A1] An example device for decoding video data includes a memory for storing the video data and one or more processors implemented in circuitry and communicatively coupled to the memory. The one or more processors are configured to determine whether a first coding unit (CU) is a skip mode CU, and based on the first CU not being a skip mode CU, determine whether the first CU is encoded using one of an intra mode or a palette mode. The one or more processors are also configured to determine whether the first CU is encoded using the palette mode based on the first CU being encoded using one of the intra mode or the palette mode. The one or more processors are also configured to decode the first CU based on the determination of whether the first CU is encoded using the palette mode.

IPC 8 full level
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AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)
BA ME

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