

Title (en)  
SIGMA-DELTA ANALOGUE-TO-DIGITAL CONVERTER WITH GMC-VDAC

Title (de)  
SIGMA-DELTA-ANALOG-DIGITAL-WANDLER MIT GMC-VDAC

Title (fr)  
CONVERTISSEUR ANALOGIQUE-NUMÉRIQUE SIGMA-DELTA AVEC GMC-VDAC

Publication  
**EP 4049372 A1 20220831 (DE)**

Application  
**EP 20797710 A 20201023**

Priority  
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• EP 2020079938 W 20201023

Abstract (en)  
[origin: WO2021078960A1] The present invention relates to a sigma-delta analogue-to-digital converter. The sigma-delta analogue-to-digital converter has a transconductance stage having a first, second and third connection. A capacitor is parallel-connected to the third connection. The sigma-delta analogue-to-digital converter also has a quantiser at the third connection of the transconductance stage with feedback by means of a voltage digital-to-analogue converter for feeding back a feedback signal to one of the connections of the transconductance stage.

IPC 8 full level  
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CPC (source: CN EP KR US)  
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Citation (search report)  
See references of WO 2021078960A1

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**DE 102019128876 A 20191025**; CN 202080075198 A 20201023; EP 2020079938 W 20201023; EP 20797710 A 20201023; JP 2022523981 A 20201023; KR 20227015018 A 20201023; US 202017755229 A 20201023