

Title (en)  
PROCESSOR FOR CONFIGURABLE PARALLEL COMPUTATIONS

Title (de)  
PROZESSOR FÜR KONFIGURIERBARE PARALLELE BERECHNUNGEN

Title (fr)  
PROCESSEUR POUR CALCULS PARALLÈLES CONFIGURABLES

Publication  
**EP 4085354 A1 20221109 (EN)**

Application  
**EP 20908751 A 20201223**

Priority  
• US 201962954952 P 20191230  
• US 2020066823 W 20201223

Abstract (en)  
[origin: US2021200710A1] A flexible processor includes (i) numerous configurable processors interconnected by modular interconnection fabric circuits that are configurable to partition the configurable processors into one or more groups, for parallel execution, and to interconnect the configurable processors in any order for pipelined operations, Each configurable processor may include (i) a control circuit; (ii) numerous configurable arithmetic logic circuits; and (iii) configurable interconnection fabric circuits for interconnecting the configurable arithmetic logic circuits.

IPC 8 full level  
**G06F 17/00** (2019.01)

CPC (source: EP KR US)  
**G06F 13/124** (2013.01 - EP KR US); **G06F 13/4022** (2013.01 - EP KR); **G06F 15/7867** (2013.01 - EP KR); **G06F 15/7871** (2013.01 - US); **G06F 15/7889** (2013.01 - EP); **G06F 15/825** (2013.01 - EP); **Y02D 10/00** (2018.01 - EP)

Designated contracting state (EPC)  
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)  
BA ME

Designated validation state (EPC)  
KH MA MD TN

DOCDB simple family (publication)  
**US 11789896 B2 20231017; US 2021200710 A1 20210701**; CN 115280297 A 20221101; EP 4085354 A1 20221109; EP 4085354 A4 20240313; JP 2023508503 A 20230302; KR 20220139304 A 20221014; US 2023418780 A1 20231228; WO 2021138189 A1 20210708

DOCDB simple family (application)  
**US 202017132437 A 20201223**; CN 202080090121 A 20201223; EP 20908751 A 20201223; JP 2022539692 A 20201223; KR 20227025841 A 20201223; US 2020066823 W 20201223; US 202318367344 A 20230912