

Title (en)

SYSTEMS AND METHODS FOR AUTOMATIC THRESHOLD SENSING FOR UVLO CIRCUITS IN A MULTI-BATTERY ENVIRONMENT

Title (de)

SYSTEME UND VERFAHREN ZUR AUTOMATISCHEN SCHWELLWERTERFASSUNG FÜR UVLO-SCHALTUNGEN IN EINER MEHRBATTERIEUMGEBUNG

Title (fr)

SYSTÈMES ET PROCÉDÉS DE DÉTECTION AUTOMATIQUE DE SEUIL POUR CIRCUITS DE VERROUILLAGE SOUS TENSION, DANS UN ENVIRONNEMENT À BATTERIES MULTIPLES

Publication

**EP 4111567 A4 20231129 (EN)**

Application

**EP 20921822 A 20200226**

Priority

US 2020019852 W 20200226

Abstract (en)

[origin: WO2021173128A1] Embodiments of the invention include an under-voltage lockout (UVLO) circuit with automatic UVLO threshold configuration. In an embodiment, the UVLO circuit includes an over-voltage protection circuit that receives power from a power source, a peak detector that detects a peak voltage output for the external power source, a voltage threshold generator that sets a UVLO threshold based on the peak voltage output, and a comparator that compares an instantaneous voltage with the UVLO threshold and configures an operating mode of a device based on the comparison.

IPC 8 full level

**H02H 9/04** (2006.01); **H02H 3/02** (2006.01); **H02H 3/20** (2006.01); **H02H 3/24** (2006.01); **H02H 7/18** (2006.01)

CPC (source: EP)

**H02H 3/006** (2013.01); **H02H 3/207** (2013.01); **H02H 3/243** (2013.01); **H02H 7/18** (2013.01); **H02H 9/041** (2013.01)

Citation (search report)

- [Y] US 2016204652 A1 20160714 - HAN LEI [US], et al
- [Y] US 2016072322 A1 20160310 - YOSHIMATSU YUSAKU [JP]
- [Y] US 8885308 B2 20141111 - WALTMAN BARRY F [US], et al
- See references of WO 2021173128A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)

**WO 2021173128 A1 20210902**; EP 4111567 A1 20230104; EP 4111567 A4 20231129

DOCDB simple family (application)

**US 2020019852 W 20200226**; EP 20921822 A 20200226