

Title (en)  
POWER MODE TRANSITION

Title (de)  
LEISTUNGSMODUSÜBERGANG

Title (fr)  
TRANSITION DE MODE DE PUISSANCE

Publication  
**EP 4121837 A1 20230125 (EN)**

Application  
**EP 20933063 A 20200428**

Priority  
US 2020030219 W 20200428

Abstract (en)  
[origin: WO2021221611A1] In various examples, a computing device may include first and second power supplies, a processor, and memory storing computer-readable instructions that cause the processor to: in response to a determination that a workload corresponding to the computing device satisfies a first criterion, transition the computing device from a redundant power mode to an aggregate power mode, wherein: in the redundant power mode, the first and second power supplies supply a first combined amount of power to the computing device, and in the aggregate power mode, the first and second power supplies supply a second combined amount of power to the computing device, wherein the second combined amount of power is greater than the first combined amount of power; and in response to a determination that the workload satisfies a second criterion, transition the computing device from the aggregate power mode to the redundant power mode.

IPC 8 full level  
**G06F 1/30** (2006.01); **G06F 1/28** (2006.01); **G06F 21/81** (2006.01)

CPC (source: EP)  
**G06F 1/263** (2013.01); **G06F 1/28** (2013.01); **G06F 1/305** (2013.01); **G06F 1/3206** (2013.01); **G06F 1/3212** (2013.01); **G06F 1/3296** (2013.01); **Y02D 10/00** (2017.12)

Citation (search report)  
See references of WO 2021221611A1

Designated contracting state (EPC)  
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)  
BA ME

Designated validation state (EPC)  
KH MA MD TN

DOCDB simple family (publication)  
**WO 2021221611 A1 20211104**; EP 4121837 A1 20230125

DOCDB simple family (application)  
**US 2020030219 W 20200428**; EP 20933063 A 20200428