

Title (en)
POWER COMBINER FOR AMPLIFIER ARRANGEMENT

Title (de)
LEISTUNGSKOMBINIERER FÜR VERSTÄRKERANORDNUNG

Title (fr)
COMBINA TEUR DE PUISSANCE POUR AGENCEMENT AMPLIFICATEUR

Publication
EP 4200980 A4 20240424 (EN)

Application
EP 20950427 A 20200819

Priority
SE 2020050799 W 20200819

Abstract (en)
[origin: WO2022039640A1] There is provided a power combiner for an amplifier arrangement. The power combiner is configured to combine an input power signal, as provided to the power combiner through outputs of power cells of the amplifier arrangement, to an output power signal of the amplifier arrangement. The power combiner comprises serially galvanically connected, and electromagnetically coupled, transmission lines arranged to combine all the outputs of power cells to, at an output of the power combiner, produce the output power signal.

IPC 8 full level
H03F 3/60 (2006.01); **H01P 5/12** (2006.01); **H03F 1/02** (2006.01); **H03F 3/195** (2006.01); **H03F 3/21** (2006.01); **H03F 3/24** (2006.01); **H03H 7/48** (2006.01)

CPC (source: EP)
H01P 5/12 (2013.01); **H03F 3/195** (2013.01); **H03F 3/211** (2013.01); **H03F 3/245** (2013.01); **H03F 3/604** (2013.01); **H03F 3/607** (2013.01); **H03F 2200/451** (2013.01)

Citation (search report)
• [XA] US 2015256132 A1 20150910 - NIIDA YOSHITAKA [JP]
• [XA] US 7276972 B2 20071002 - KOSUGI MAKOTO [JP], et al
• [XI] YU-JIU WANG ET AL: "A compact low-noise weighted distributed amplifier in CMOS", SOLID-STATE CIRCUITS CONFERENCE - DIGEST OF TECHNICAL PAPERS, 2009. ISSCC 2009. IEEE INTERNATIONAL, IEEE, PISCATAWAY, NJ, USA, 8 February 2009 (2009-02-08), pages 220, XP031742236, ISBN: 978-1-4244-3458-9
• [XI] TAKESHI ITO ET AL: "A DC-7 GHz Small-Area Distributed Amplifier Using 5-port Inductors in a 180nm Si CMOS Technology", SOLID-STATE CIRCUITS CONFERENCE, 2006. ASSCC 2006. IEEE ASIAN, IEEE, PI, 2 November 2006 (2006-11-02), pages 363 - 366, XP031086830, ISBN: 978-0-7803-9735-4
• [A] ENTESARI K ET AL: "CMOS Distributed Amplifiers With Extended Flat Bandwidth and Improved Input Matching Using Gate Line With Coupled Inductors", IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES, IEEE, USA, vol. 36, no. 12, 2 December 2009 (2009-12-02), pages 2862 - 2871, XP011284078, ISSN: 0018-9480
• See also references of WO 2022039640A1

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

DOCDB simple family (publication)
WO 2022039640 A1 20220224; EP 4200980 A1 20230628; EP 4200980 A4 20240424

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