

Title (en)

POWER SUPPLY CIRCUIT, CONTROLLING METHOD, LIGHTING DEVICE DRIVER AND LIGHTING EQUIPMENT

Title (de)

STROMVERSORGUNGSSCHALTUNG, STEUERUNGSVERFAHREN, BELEUCHTUNGSVORRICHTUNGSTREIBER UND BELEUCHTUNGSAUSRÜSTUNG

Title (fr)

CIRCUIT D'ALIMENTATION ÉLECTRIQUE, PROCÉDÉ DE COMMANDE, CIRCUIT D'ATTAQUE DE DISPOSITIF D'ÉCLAIRAGE ET ÉQUIPEMENT D'ÉCLAIRAGE

Publication

**EP 4252341 A4 20240214 (EN)**

Application

**EP 21918513 A 20210115**

Priority

CN 2021071997 W 20210115

Abstract (en)

[origin: WO2022151305A1] A power supply circuit, a controlling method and a lighting equipment. an input circuit, configured to receive input power and convert the input power into a first direct current (DC) power, the input circuit comprising an input PFC (Power Factor Correction) circuit and a transformer, the input PFC circuit receiving the input power, a primary winding of the transformer being connected to the input PFC circuit, a secondary winding of the transformer outputting the first direct current power; an output regulator, configured to convert the first direct current power into a second direct current power, the second direct current power being used to drive an electric equipment; a controller, configured to output a controlling signal via an output pin according to a first detecting signal or a second detecting signal, the controlling signal being used to generate a feedback signal which is inputted to a feedback pin of a PFC chip of the PFC circuit, the first detecting signal being generated according to status of the input power, the second detecting signal being generated according to status of the output regulator, when failure status of the output regulator is detected, the PFC chip is controlled to lower down the second direct current power, when the input power is DC power, the PFC chip is controlled to lead the second direct current power into a jitter mode.

IPC 8 full level

**H02M 1/42** (2007.01); **H02M 1/00** (2006.01); **H02M 1/44** (2007.01); **H02M 3/335** (2006.01); **H04B 1/69** (2011.01); **H05B 45/382** (2020.01)

CPC (source: EP)

**H02M 1/0035** (2021.05); **H02M 1/007** (2021.05); **H02M 1/4225** (2013.01); **H02M 1/4258** (2013.01); **H02M 1/44** (2013.01); **H02M 3/33507** (2013.01); **H05B 45/355** (2020.01); **H05B 45/50** (2020.01); **H05B 45/327** (2020.01); **Y02B 70/10** (2013.01)

Citation (search report)

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- [A] EP 2315497 A1 20110427 - NXP BV [NL]
- [A] WO 2020254003 A1 20201224 - SIGNIFY HOLDING BV [NL]
- [A] DE 102018203599 A1 20190912 - OSRAM GMBH [DE]
- [A] US 2013193849 A1 20130801 - ZIMMERMANN MICHAEL [CH], et al
- [A] WO 2013186227 A2 20131219 - IKON SEMICONDUCTOR LTD [IE]
- See also references of WO 2022151305A1

Designated contracting state (EPC)

AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)

BA ME

Designated validation state (EPC)

KH MA MD TN

DOCDB simple family (publication)

**WO 2022151305 A1 20220721**; CN 116711201 A 20230905; EP 4252341 A1 20231004; EP 4252341 A4 20240214

DOCDB simple family (application)

**CN 2021071997 W 20210115**; CN 202180085760 A 20210115; EP 21918513 A 20210115