

Title (en)

SEQUENTIAL BURST-MODE AUTOMATIC GAIN/OFFSET CONTROL SYSTEM FOR TRANSIMPEDANCE AMPLIFIERS

Title (de)

SYSTEM ZUR SEQUENTIELLEN BURST-MODUS-STEUERUNG DER VERSTÄRKUNG/OFFSET FÜR TRANSIMPEDANZVERSTÄRKER

Title (fr)

SYSTÈME DE COMMANDE AUTOMATIQUE DE GAIN/DÉCALAGE SÉQUENTIEL EN MODE RAFALE POUR AMPLIFICATEURS DE TRANSIMPÉDANCE

Publication

EP 4256701 A1 20231011 (EN)

Application

EP 21762035 A 20210816

Priority

EP 2021072747 W 20210816

Abstract (en)

[origin: WO2023020680A1] Circuitry (400) for controlling the amplification of an input signal (408), the circuitry (400) comprising: an amplifier (401) for receiving an input signal (408) and being configured to amplify the input signal (408) to generate an amplified signal (409); a cascading series of N comparison blocks (403), C1, C2, ... Cn-1, Cn, ... CN, each comparison block (403), Cn, being for receiving a reset input signal (412) from a previous comparison block (403), Cn-1, and being configured to receive the amplified signal (409); wherein each comparison block (403) is further configured to control its state in dependence on the reset input signal (412) and the amplified signal (409). In this way, the control of each comparison block (403), and consequently the control of the amplifier (401), may be dependent on the reset signal (413) from the previous comparison block (403) in the cascading series of comparison blocks (403).

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

See references of WO 2023020680A1

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