Title (en)
METHOD FOR ADDRESS CONFIGURATION FOR A MASTER-SLAVE SYSTEM

Title (de)
VERFAHREN ZUR ADRESSKONFIGURATION FÜR EIN MASTER/SLAVE-SYSTEM

Title (fr)
PROCÉDÉ DE CONFIGURATION D'ADRESSE POUR UN SYSTÈME MAÎTRE-ESCLAVE

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Abstract (en)
[origin: WO2022128618A1] The invention relates to a method for address configuration for a master-slave system by means of a field bus (3) for serial data transmission, wherein a master unit (1) is connected to the field bus (3). The method is characterized in that a number range for addresses of slave units on the field bus (3) is divided into a static range and a dynamic range; in that at least one new slave unit (2a-f) is connected to the field bus (3); in that, into each of the at least one new slave units (2a-f), an initial field bus address (4a-f) from the dynamic range is programmed, by means of which initial field bus address (4a-f) the at least one new slave unit (2a-f) in question can be addressed; in that the master unit (1) queries the dynamic range of the addresses of slave units; in that the master unit (1) receives a reply message from each of the at least one new slave units (2a-f) in response to the query; in that the master unit (1) transmits a desired field bus address (9) from the static range to the at least one new slave unit (2a-f) so that the desired field bus address (9) is programmed into the at least one new slave unit (2a-f) in place of the initial field bus address (4a-f) and so that the at least one new slave unit (2a-f) can be addressed by means of the desired field bus address (9). The invention also relates to an additional method for address configuration for a master-slave system by means of a field bus, to a master unit for a master-slave system and to a master-slave system having a field bus.

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