

Title (en)
HIGH ELECTRON MOBILITY TRANSISTOR (HEMT), TRANSISTOR ARRANGEMENT, METHOD OF CONTROLLING AN HEMT AND METHOD OF PRODUCING AN HEMT

Title (de)
TRANSISTOR MIT HOHER ELEKTRONENBEWEGLICHKEIT (HEMT), TRANSISTORANORDNUNG, VERFAHREN ZUM STEUERN EINES HEMTS UND VERFAHREN ZUM HERSTELLEN EINES HEMTS

Title (fr)
TRANSISTOR À HAUTE MOBILITÉ ÉLECTRONIQUE (HEMT), ENSEMBLE TRANSISTOR, PROCÉDÉ DE COMMANDE D'UN HEMT ET PROCÉDÉ DE FABRICATION D'UN HEMT

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Application
EP 22712307 A 20220223

Priority
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Abstract (en)
[origin: WO2022180127A1] A high electron mobility transistor (HEMT) having a first layer (110) and a second layer (120) is described. The first layer includes a first material (111) composed of a first nitride compound. The first nitride compound includes a group III element. The second layer includes a second material (121) composed of a second nitride compound. The second nitride compound includes a group III element. A main surface (122) of the second layer is arranged opposite a main surface (112) of the first layer, such that a charge zone (160) is formed along the main surface of the second layer. The HEMT also has a gate electrode (170) arranged opposite at least regions of the second layer, such that the second layer is disposed between the first layer and the gate electrode. In addition, the HEMT has a third layer (130) disposed between the gate electrode and the second layer. The third layer includes a ferroelectric third material (131) composed of a third nitride compound, or a ferroelectric third material composed of an oxide compound including zinc.

IPC 8 full level
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CPC (source: EP US)
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Citation (search report)
See references of WO 2022180127A1

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