

Title (en)
DYNAMIC IRC & ELVSS FOR DISPLAY DEVICE

Title (de)
DYNAMISCHE IRC UND ELVSS FÜR EINE ANZEIGEVORRICHTUNG

Title (fr)
IRC ET ELVSS DYNAMIQUES POUR DISPOSITIF D'AFFICHAGE

Publication
EP 4302290 A1 20240110 (EN)

Application
EP 21729707 A 20210510

Priority
US 2021031630 W 20210510

Abstract (en)
[origin: WO2022240389A1] A method, includes: (i) receiving information about an ambient light level; (ii) receiving image frame data for an active matrix display panel with an array of pixels each having a light emitting diode (LED) and a pixel circuit to control current supplied to the LED; (iii) selecting a selected current-resistance compensation (IRC) setting based on the information about the ambient light value; (iv) selecting a selected source voltage level based on the selected IRC setting that was selected by the computing system; (v) generating compensated image frame data for the image frame based on the received image frame data and the selected IRC setting; and (vi) displaying the image frame by supplying data signals based on the compensated image frame data to corresponding pixels from the array of pixels, while applying a source voltage corresponding to the selected source voltage level to all of the pixels.

IPC 8 full level
G09G 3/3233 (2016.01)

CPC (source: EP US)
G09G 3/3233 (2013.01 - EP); **G09G 3/3258** (2013.01 - US); **G09G 3/3406** (2013.01 - US); **G09G 2320/0223** (2013.01 - EP); **G09G 2330/028** (2013.01 - EP); **G09G 2360/144** (2013.01 - EP US); **G09G 2360/16** (2013.01 - EP)

Designated contracting state (EPC)
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO RS SE SI SK SM TR

Designated extension state (EPC)
BA ME

Designated validation state (EPC)
KH MA MD TN

DOCDB simple family (publication)
WO 2022240389 A1 20221117; CN 117043840 A 20231110; EP 4302290 A1 20240110; US 11908416 B2 20240220; US 2023306912 A1 20230928

DOCDB simple family (application)
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